

1200V 2A N-Channel Enhancement Mode Power MOSFET

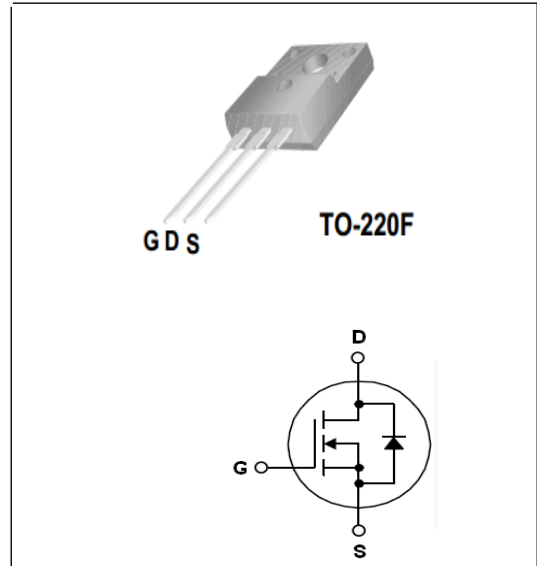
Description

The AKQPF2N120CL is an N-Channel on-resistance and high avalanche energyenhancement mode power MOSFET which using proprietary planar stripe and DMOS technology.

This MOSFET has low static strength. This device provide excellent switching performance for switched mode power supplies, active power factor correction and electronic lamp ballasts.

Features

- Low on-Resistance: $R_{DS(on)}=5.35\Omega(\text{typ.})$
- Special Process Technology for high ESD Capability
- 100% Avalanche Test
- Good Stability and Uniformity with High E_{AS}



Applications

- Switched Mode Power Supplies
- Active Power Factor Correction, Electronic Ballasts

Absolute Maximum Ratings @ $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain to Source Voltage	1200	V
V_{GSS}	Gate to Source Voltage	± 30	V
I_D	Drain Current	$T_C=25^\circ\text{C}$	2
		$T_C=100^\circ\text{C}$	1.2
I_{DM}	Pulsed Drain Current (Note1)	8	A
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	110
	Derate above 25°C		0.85
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	70	mJ
T_J	Operating Junction Temperature Range	-55~+150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55~+150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Ratings	Unit
$R_{th(J-C)}$	Thermal Resistance, Junction to case	1.1	$^\circ\text{C}/\text{W}$
$R_{th(J-A)}$	Thermal Resistance, Junction to Ambient	62.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics @T_c=25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	1200	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	3.0	-	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =1A	-	5.35	5.8	Ω
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =V _{DSS} , V _{GS} =0V	-	-	10	uA
I _{GSS}	Gate to Source Leakage Current	V _{GS} =V _{GSS} , V _{DS} =0V	-	-	±100	nA

D-S Diode Characteristics and Maximum Rating @T_c=25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Maximum Drain to Source Diode Forward Current		-	-	2.0	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} =0V, I _S =2A	-	-	0.85	V
T _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =2A,	-	400	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt=-100A/us	-	3.3	-	nC

Switching Characteristics @T_c=25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
t _{d(on)}	Turn-on Delay Time	I _D =2A, V _{DD} =600V, R _G =25Ω (Note 3)	-	35	-	ns
t _r	Rise Time		-	45	-	ns
t _{d(off)}	Turn-off Delay Time		-	47	-	ns
t _f	Fall Time		-	42	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, f=1.0MHz	-	810	-	pF
C _{oss}	Output Capacitance		-	67	-	pF
C _{rss}	Reverse Transfer Capacitance		-	5.1	-	pF
Q _g	Total Gate Charge	I _D =2A, V _{DD} =960V V _{GS} =10V (Note 3)	-	16	-	nC
Q _{gs}	Gate to Source Charge		-	4.4	-	nC
Q _{gd}	Gate to Drain Charge		-	7.8	-	nC

Note:

1. Repetitive rating: pulse-width limited by maximum junction temperature
2. V_{DD}=100V, L=10mH, R_G=25Ω, V_G=10V, stating T_J=25°C
3. Essentially independent of operating temperature typical characteristics

Typical Performance Characteristics

Fig. 1. Typical on-Region Characteristics

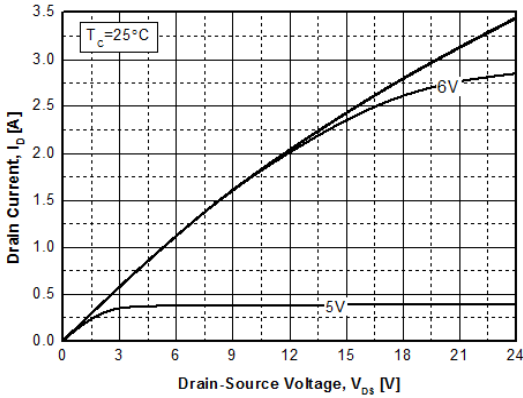


Fig. 2. Typical Transfer Characteristics

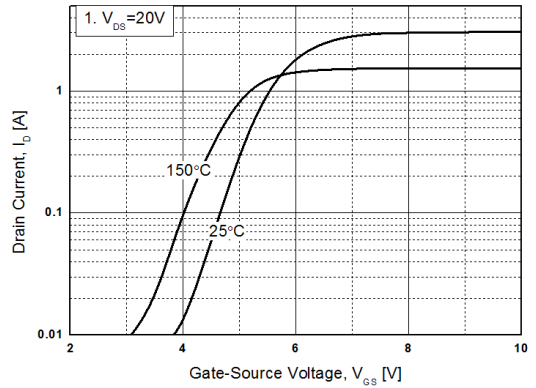


Fig. 3. Static on-Resistance vs. I_D

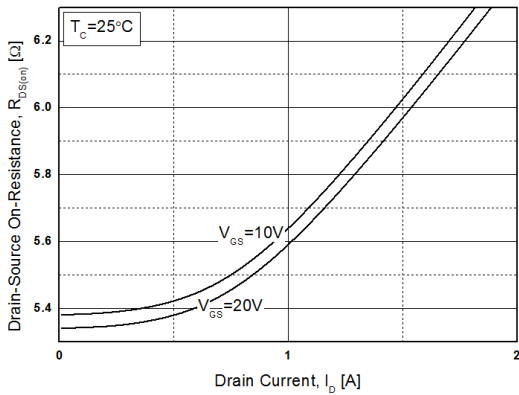


Fig. 4. Body Diode Forward Voltage vs. I_{DR}

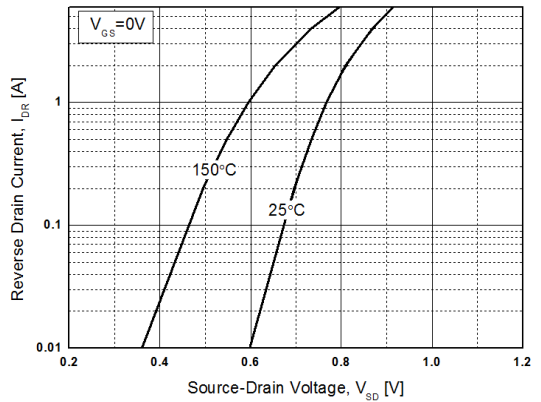


Fig. 5. Capacitance Characteristics

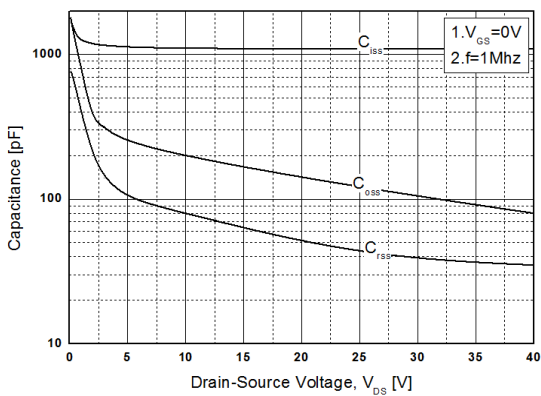
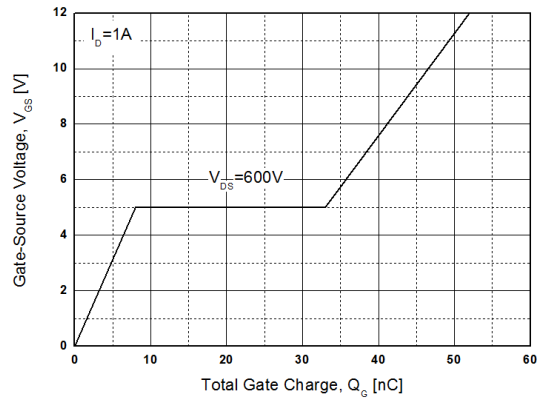


Fig. 6. Gate Charge Characteristics



Typical Performance Characteristics

Fig. 7. Breakdown Voltage vs. Temperature

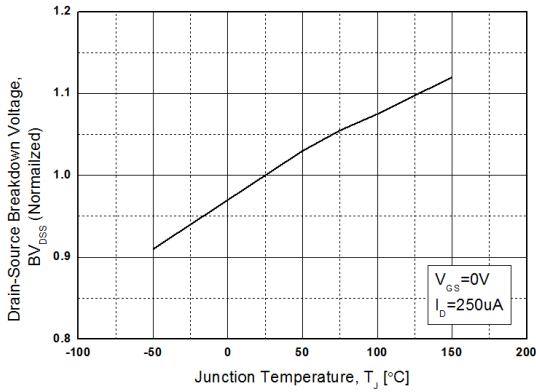


Fig. 8. Static on-Resistance vs. Temperature

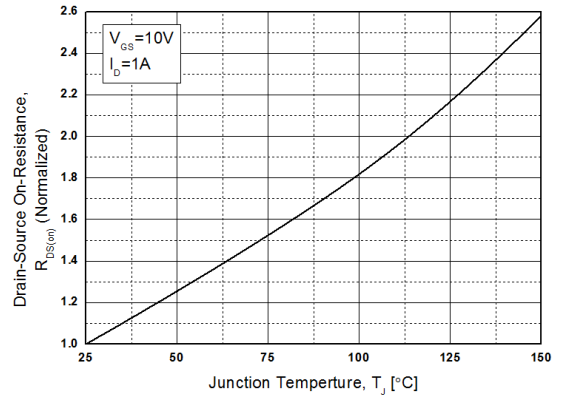


Fig. 9. Maximum Safe Operating Area

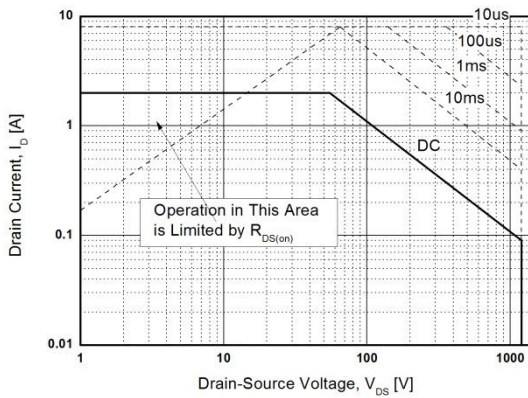


Fig. 10. Maximum Drain Current vs. Temperature

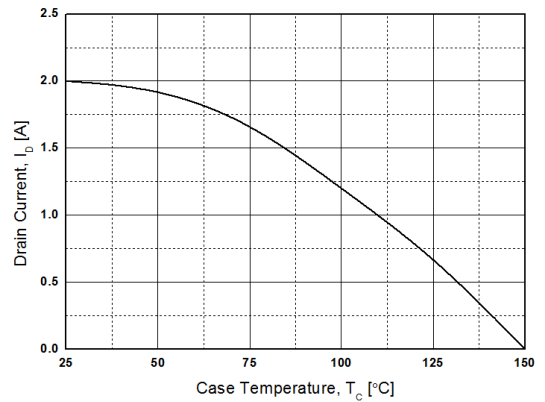
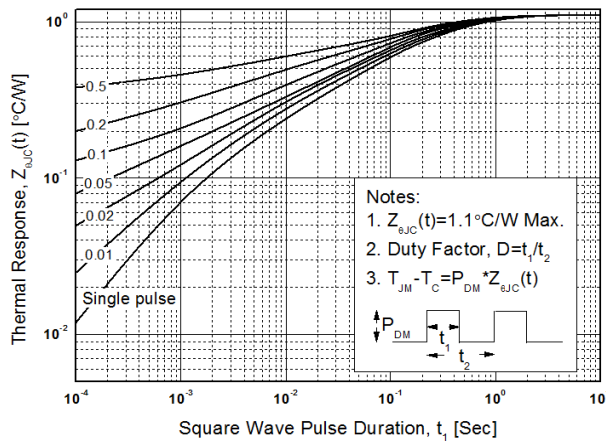


Fig. 11. Transient Thermal Response Curve



Package Dimensions

TO-220F

(Dimensions in Millimeters)

